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## APPLICANTS

Keiji Hayashida, Kawasaki, JAPAN;

Atsushi Hasegawa, Kawasaki, JAPAN;

\*\* CONTINUING DATA \*\*\*\*\* *lbt nov*\*\* FOREIGN APPLICATIONS \*\*\*\*\* *lbt*  
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## IF REQUIRED, FOREIGN FILING LICENSE GRANTED

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Foreign Priority claimed 35 USC 119 (a-d) conditions met	<input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance	STATE OR COUNTRY JAPAN	SHEETS DRAWING 6	TOTAL CLAIMS 11	INDEPENDENT CLAIMS 2
Verified and Acknowledged <i>lbt</i>	Examiner's Signature <i>lbt</i> Initials				

## ADDRESS

23373  
 SUGHRUE MION, PLLC  
 2100 PENNSYLVANIA AVENUE, N.W.  
 SUITE 800  
 WASHINGTON , DC  
 20037

## TITLE

Phase-locked loop circuit reducing steady state phase error

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